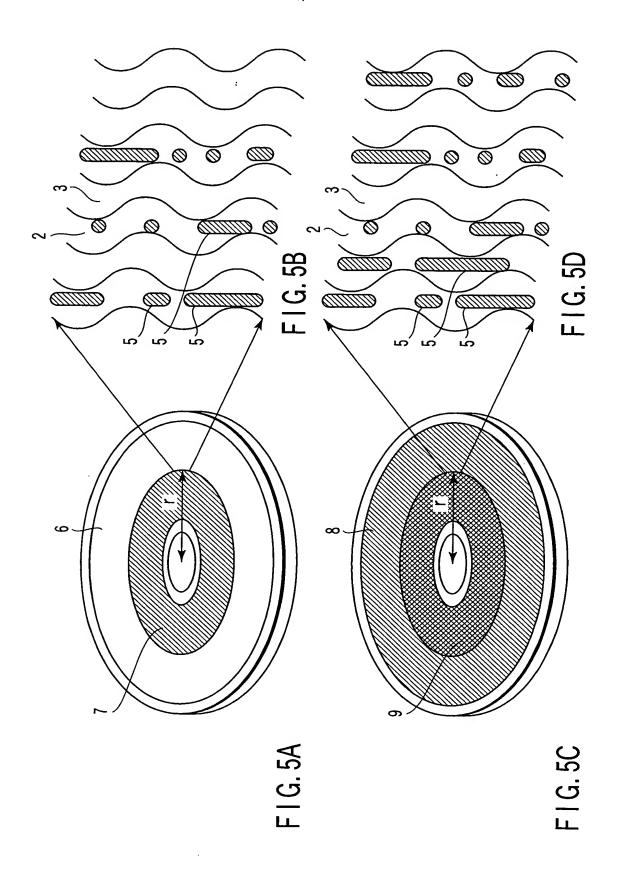
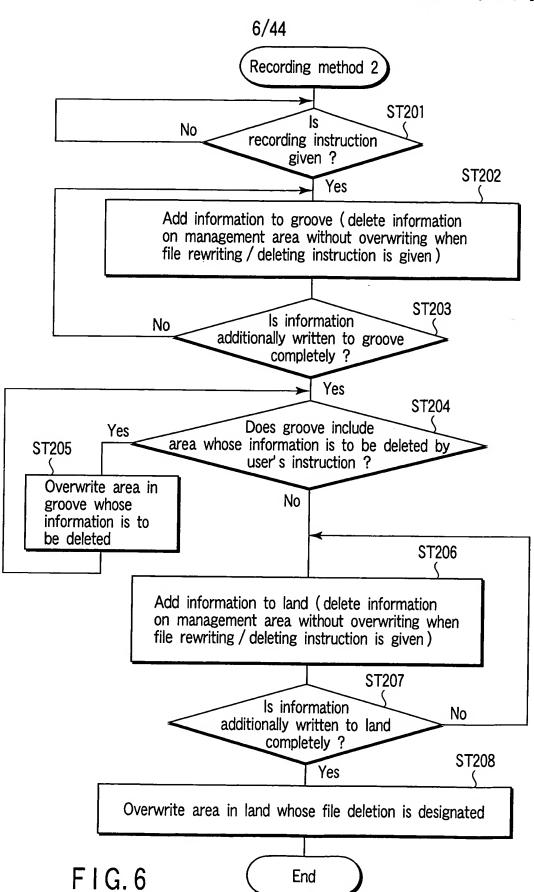
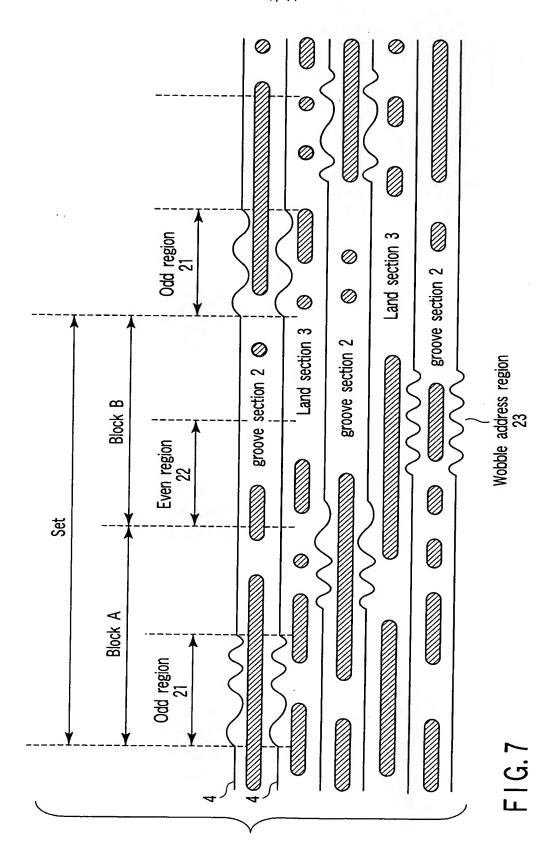


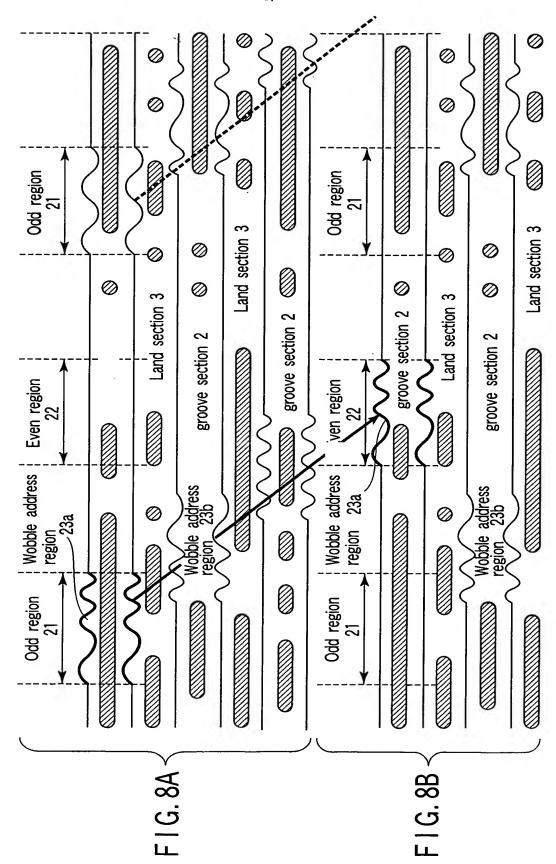
FIG.4

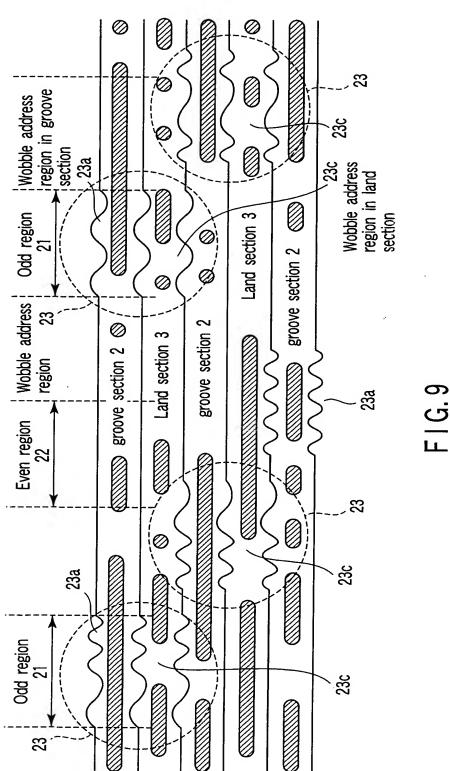




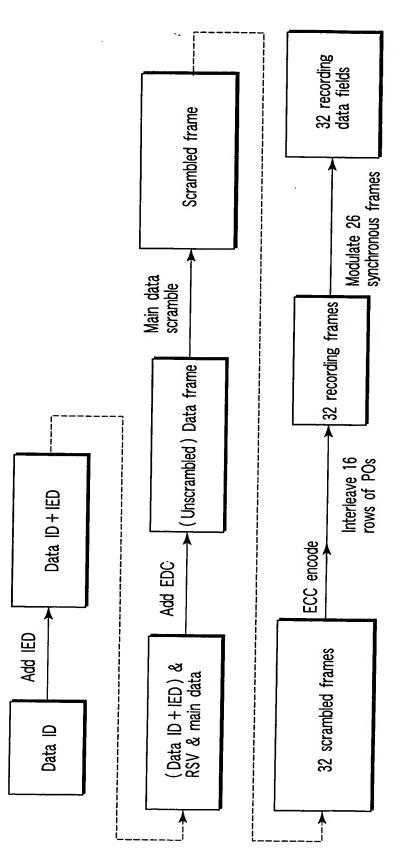
AND A CONTRACTOR STATE











F1G. 10

_	<u> </u>			T	1	Τ		1
							EBC	4 hytes
172 bytes		Main data 172 bytes (D160~D331)	main data 172 bytes (D504~D675)	main data 172 bytes (D848~D1019)	main data 172 bytes (D1192~D1363)	main data 172 bytes (D1536~D1707)	main data 172 bytes (D1880~D2047)	
172 bytes	6 bytes	RSV Main data (D0~D159)	ata 172 bytes (D332~D503)	ata 172 bytes (D676~D847)	ata 172 bytes (D1020~D1191)	ata 172 bytes (D1364~D1535)	uta 172 bytes (D1708~D1879)	
	$\begin{vmatrix} 2 \\ 4 \end{vmatrix}$ bytes	data IED ID	main data	main data	main data	main data 1	main data 1	
_\\	4	<u> </u>		y rows				

F 6.1

SB)		12	2/44
b0 (LSB)		b24	Layer number
		625	Data type
	Data field number	b27 b26	Area type
b23		b28	Recording type
b24	ion	b29	Reflectance
	Data field information	p30	Tracking method
(MSB) b31	Da	b31	Sector format type
SW)			

FIG. 12

Area	Contents			
Embossed data zone	Sector number			
Defect management area	Sector number			
Disk identification zone	Sector number			
Block used in data area	LSN + 031000h			
Block unused in data area	One of state 1: first 3 bits are 0 and number is subsequently incremented, state 2: between 00 0000h and 00 00Hh, and state 3: Unrecorded			

F I G. 13

	Area	Contents	
Emboss	ed data zone	Reserved	
Rewritable	Lead-in area, lead-out area	Reserved	
data zone	Data area	0b : General data 1b : Real-time data	

F I G. 14

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14/44

]
Initial preset value	0010h	5000h	0020h	2001h	0040h	4002h	40800	0005h	
Initial preset number	- W	9h	0Ah	0Bh	OCh	0Dh	0Eh	0Fh	shift register
Initial preset value	0001h	5500h	0002h	2A00h	0004h	5400h	0008h	2800h	Initial value in shift register
Initial preset number	Oh	1h	2h	3h	4h	5h	6h	7h	
		1							7

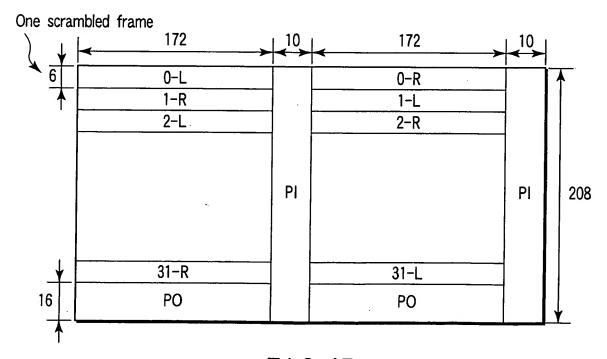
Feedback shift register

F1G. 15B

15/44

_		← 172	? bytes ->	← 10-	byte PI→	172	bytes ->	 	byte PI —
	A	B0,0	B0,171	B0,172	B0,181	B0,182	B0,353	B0,354	B0,363
		B1,0	B1,171	B1,172	B1,181	B1,182	B1,353	B1,354	B1,363
		B2,0	B2,171	B2,172	B2,181	B2,182	B2,353	B2,354	B2,363
192 rd	ows								
		B189,0	B189,171	B189,172	B189,181	B189,182	B189,353	B189,354	B189,363
		B190,0	B190,171	B190,172	B190,181	B190,182	B190,353	B190,354	B190,363
_		B191,0	B191,171	B191,172	B191,181	B191,182	B191,353	B191,354	B191,363
16 rov	.,. N	B192,0	B192,171	B192,172	B192,181	B192,182	B192,353	B192,354	B192,363
of PO									
-	<u> </u>	B207,0	B207,171	B207,172	B207,181	B207,182	B207,353	B207,354	B207,363

FIG. 16



F I G. 17

16/44

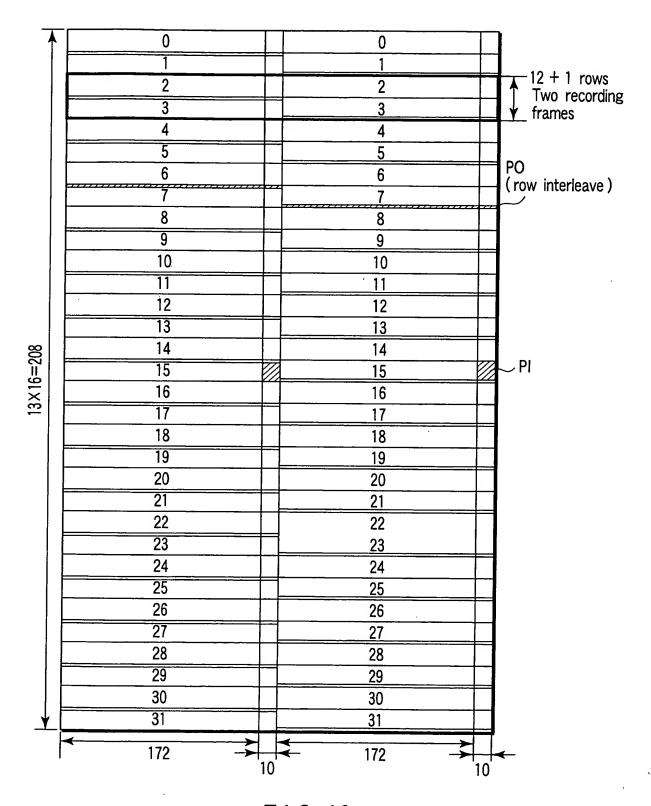
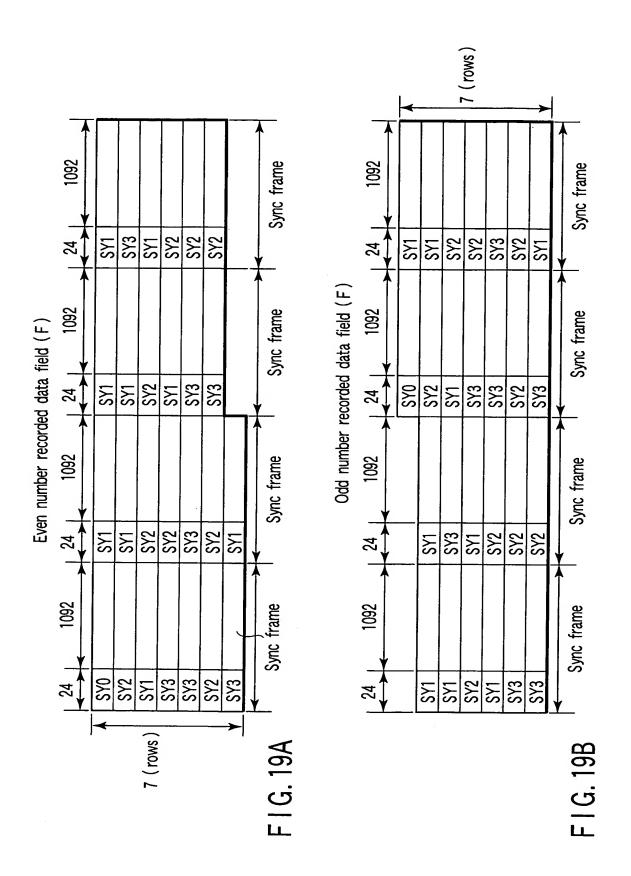


FIG. 18

17/44



	10	-	-				-	=	=	=	
e (LSB)	00100	001001	001001	001001		e (LSB)	001001	00100	001001	00100	
(MSB) Second SYNC code (LSB)	000000	000000	000000	000000		(MSB) Second SYNC code (LSB)	000000	000000	000000	000000	
MSB) Secon	001000	101000	101000	001000		MSB) Secon	001000	101000	001000	001000	
	000010	100010	101000	101010)	000100	001010	010000	010101	
	_	_	_	_			_	_	_	\	Sync code
LSB)	001001	001001	001001	001001		LSB)	001001	001001	001001	001001	Syn
MSB) First SYNC code (LSB)	000000	000000	000000	000000		MSB) First SYNC code (LSB)	000000	000000	000000	000000	
ASB) First S	101000	001000	001000	001000		(SB) First S	101000	001000	101000	001000	
	000010	100001	100100	101000		2	000100	001001	010000	010100	0
	11	II	11	Н			11	11	II	11	5.2
	SY0	SY1	SY2	SY3	State 1		SY0	SY1	SY2	SY3	F1G.20

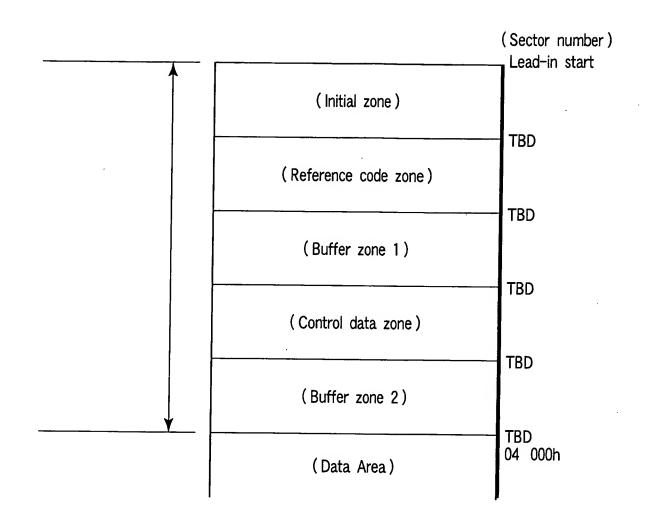
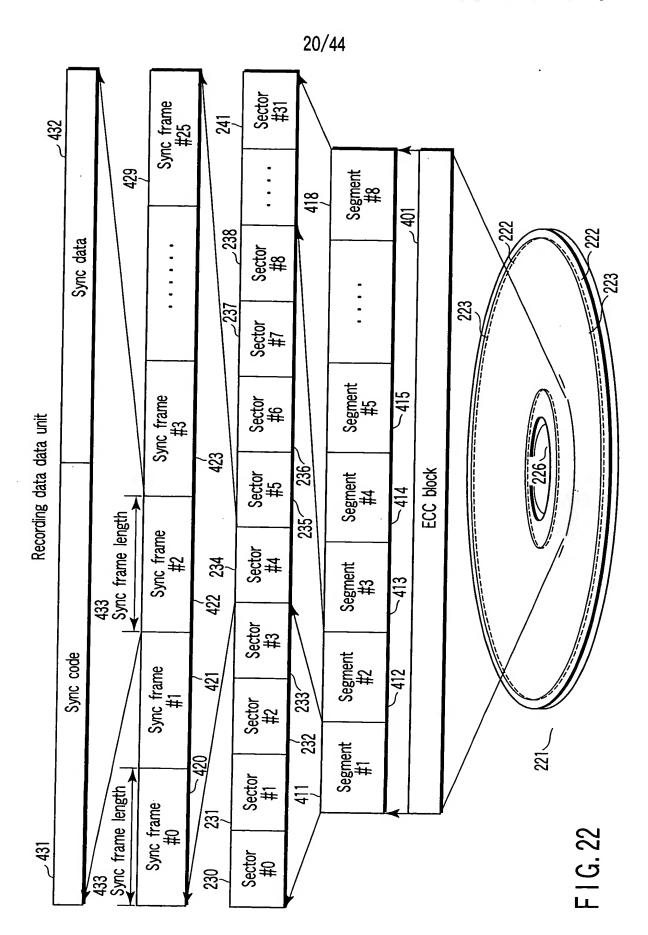
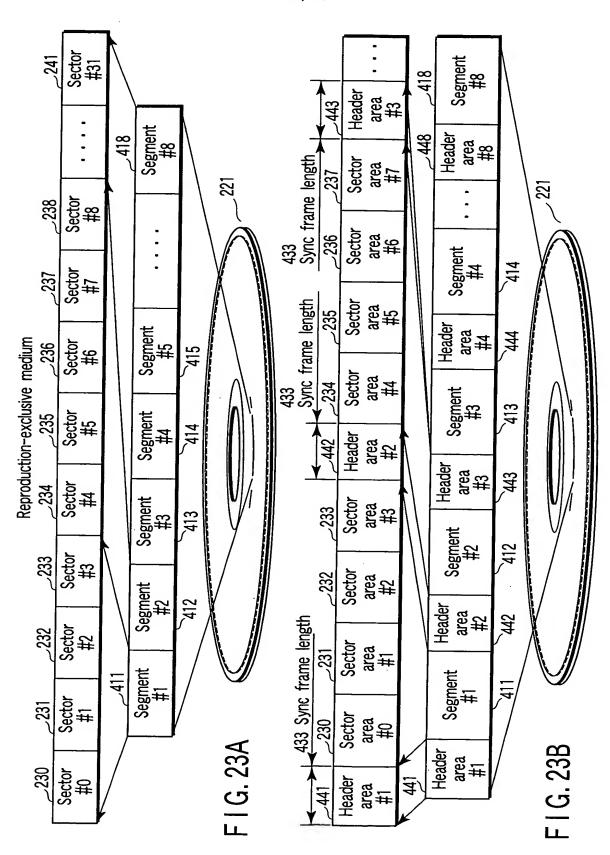


FIG. 21



1.55



		_			22/44					
418	Segment #8		Segment #8	418		Segment #8	418		Segment #8	418
	•	433 Sync frame length	Reproduction- exclusive header area #8	448	433 Sync frame length	Postscript type header area #8	458	433 Sync frame length	Rewritable header area #8	7468
	:		:			•	į		:	94.
~415	Segment #5		Segment #3	413		Segment #3	713		Segment #3	413
~414	Segment #4	433 Sync frame length	Reproduction- exclusive header area #3	443	433 Sync frame length	Postscript type header area #3	453	433 Sync frame length	Rewritable header area #3	7463
_413	Segment #3		Segment #2	412		Segment #2	412		Segment #2	412
7412	Segment #2	433 Sync frame length	Reproduction- exclusive header area #2	747	433 Sync frame length	Postscript type header area #2	452	433 Sync frame length	Rewritable header area #2	7462
411	Segment #1		Segment #1	411		Segment #1	411		Segment #1	411
_	F1G.24A		F1G.24B			F1G.24C		\	F1G.24D	

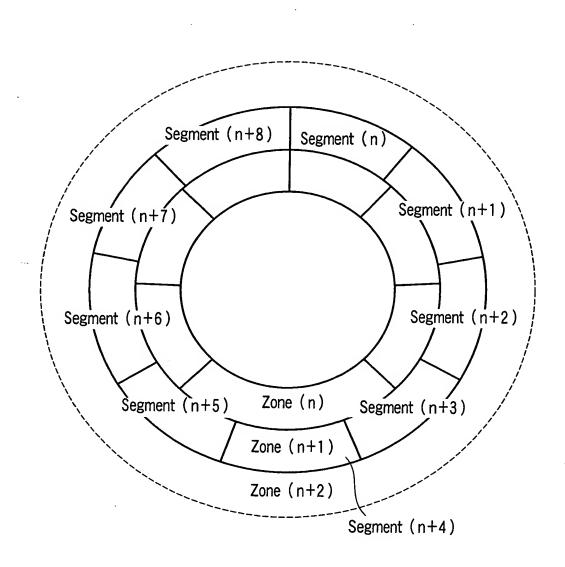
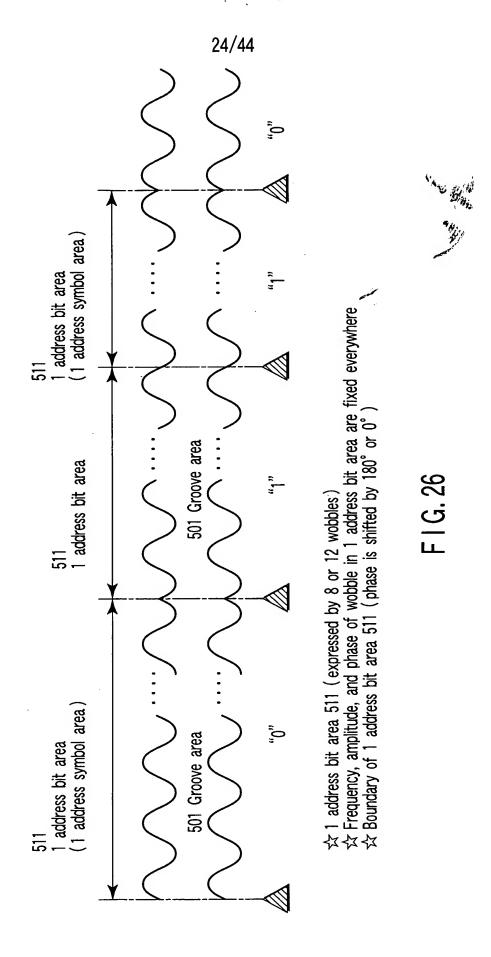
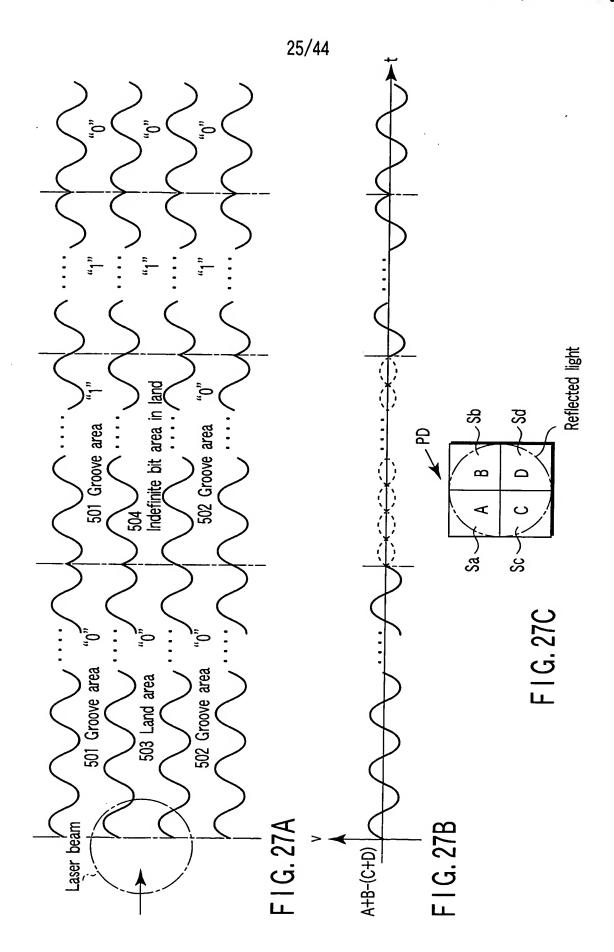


FIG. 25



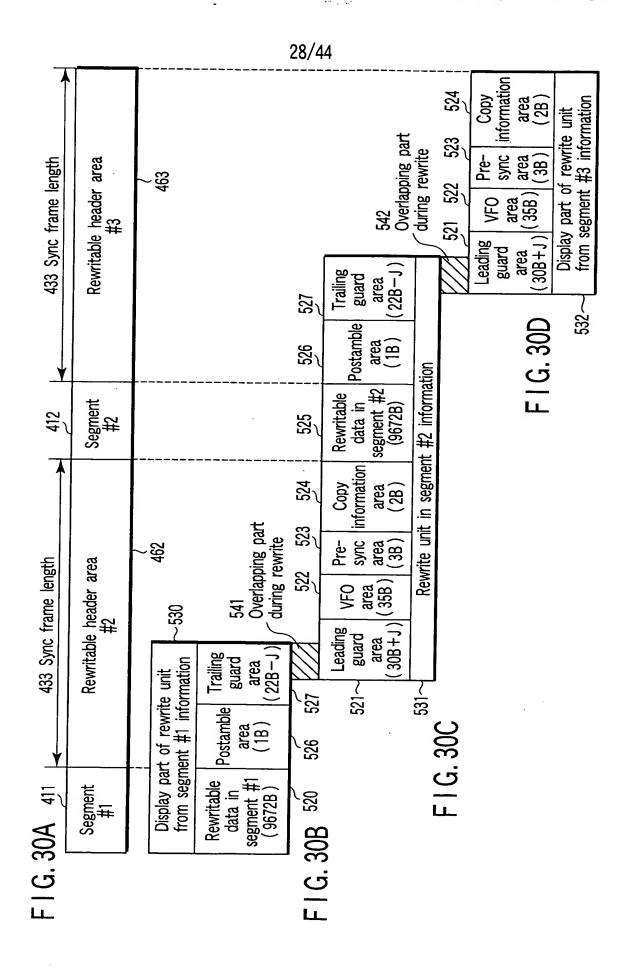


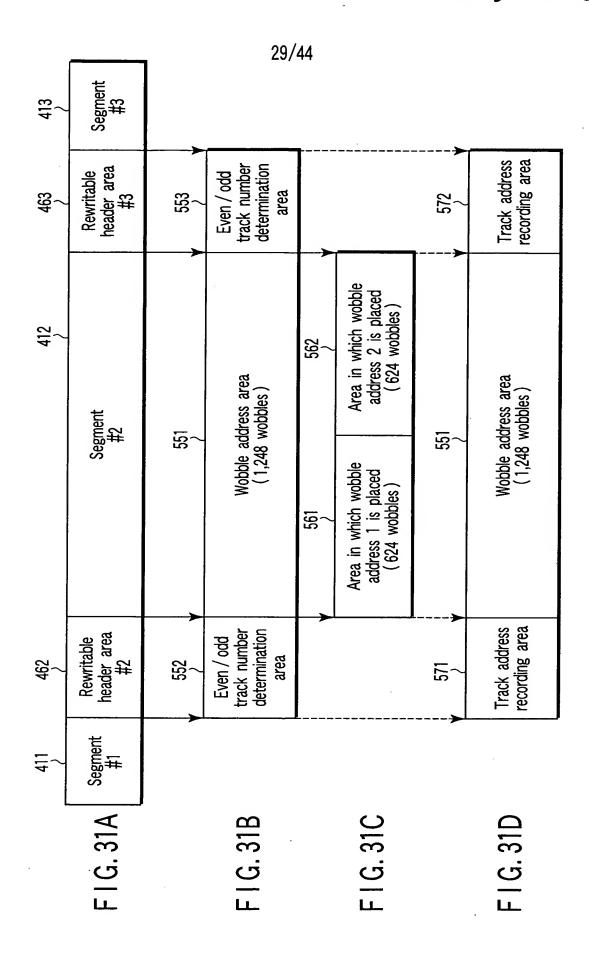
Decimal number	Conventional binary notation	Gray code notation			
0	0000	0000			
1	0001	0001			
2	0010	0011			
3	0011	0010			
4	0100	0110			
5	0101	0111			
6	0110	0101			
7	0111	0100			
8	1000	1100			
9	1001	1101			
10	1010	1111			
11	1011	1110			
12	1100	1010			
13	1101	1011			
14	1110	1001			
15	1111	1000			

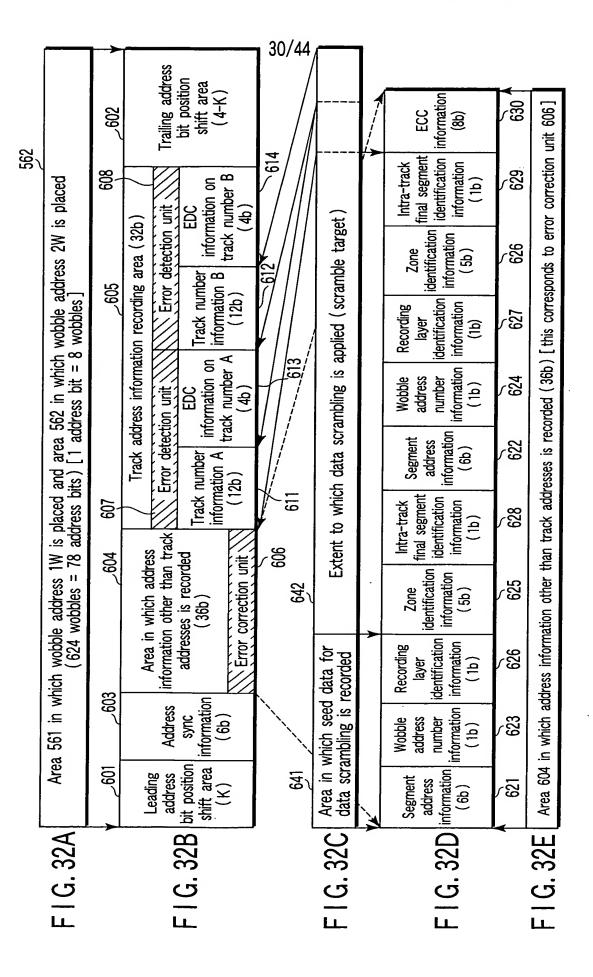
F I G. 28

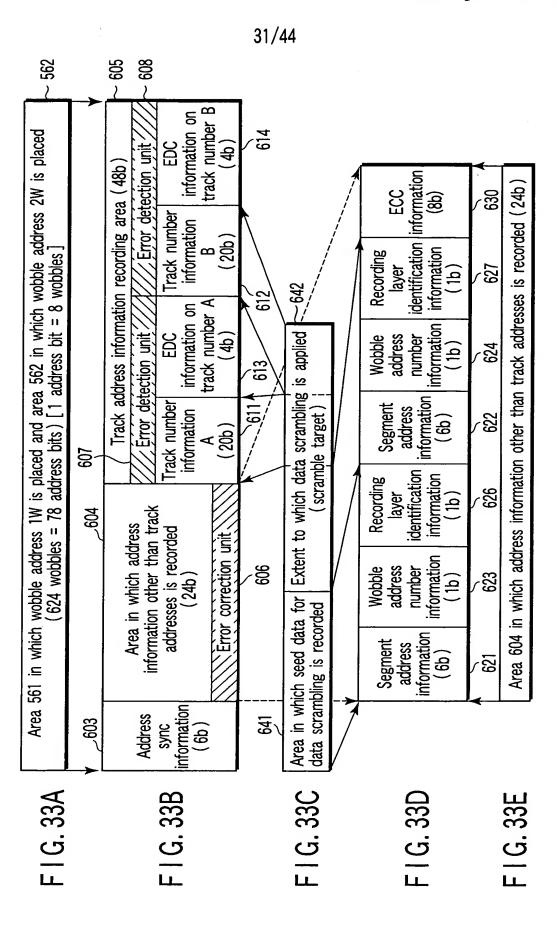
Special track code	10 ··· 00000	10 ··· 00001	10 ··· 00011	10 ··· 00010	10 00110	10 00111	10 00101	10 00100	10 01100	10 01101	10 01111	10 01110	10 01010	10 01011	10 ··· 01001	10 ··· 01000
Conventional binary notation	00001	00011	00101	00111	01001	01011	01101	01111	10001	10011	10101	10111	11001	11011	11101	11111
Decimal number	_	က	2	7	6	11	13	15	11	19	21	23	25	12	59	31
Special track code	00000 00	00 ··· 00001	00 ··· 00011	00 ··· 00010	00 ··· 00110	00 ··· 00111	00 ··· 00101	00 00100	00 ··· 01100	00 ··· 01101	00 ··· 01111	00 ··· 01110	00 ··· 01010	00 ··· 01011	00 ··· 01001	00 01000
Conventional binary notation	00000	00010	00100	00110	01000	01010	01100	01110	10000	10010	10100	10110	11000	11010	11100	11110
Decimal number	0	2	7	9	∞	10	12	14	16	18	20	22	24	76	28	30

Note] "2n" and "2n+1" differ only in most significant bit. All other lower bits are the same.









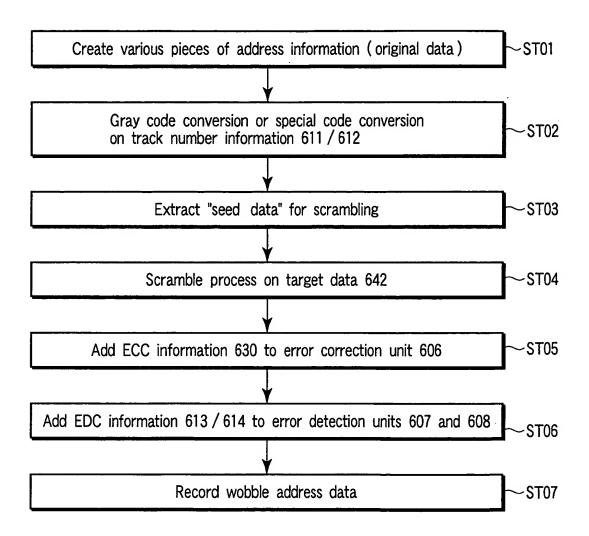


FIG. 34

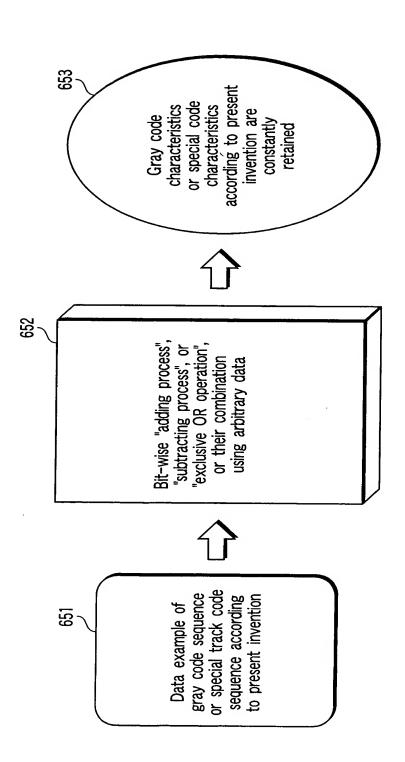
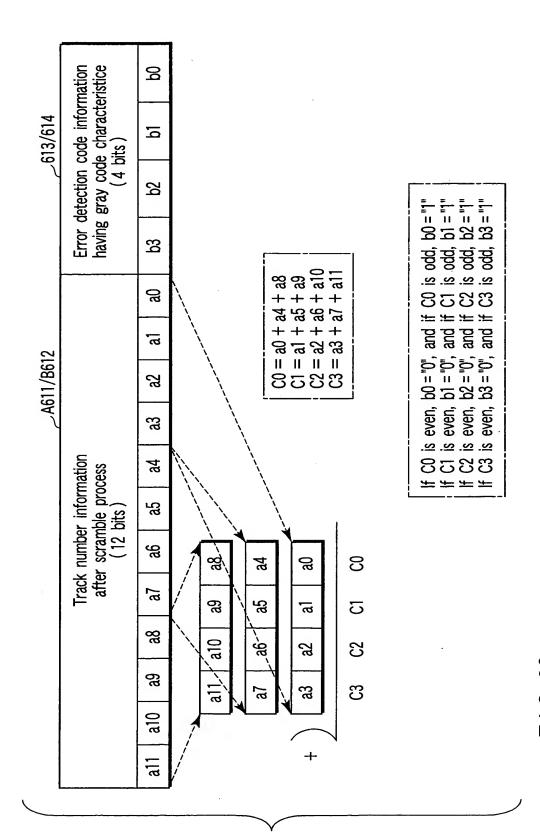
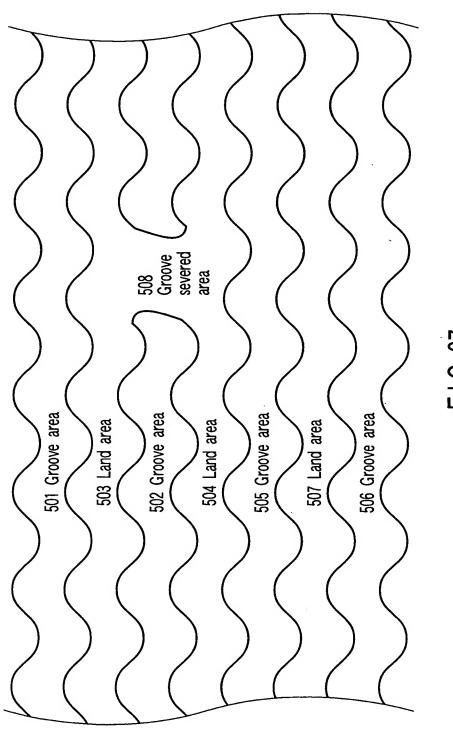


FIG. 35



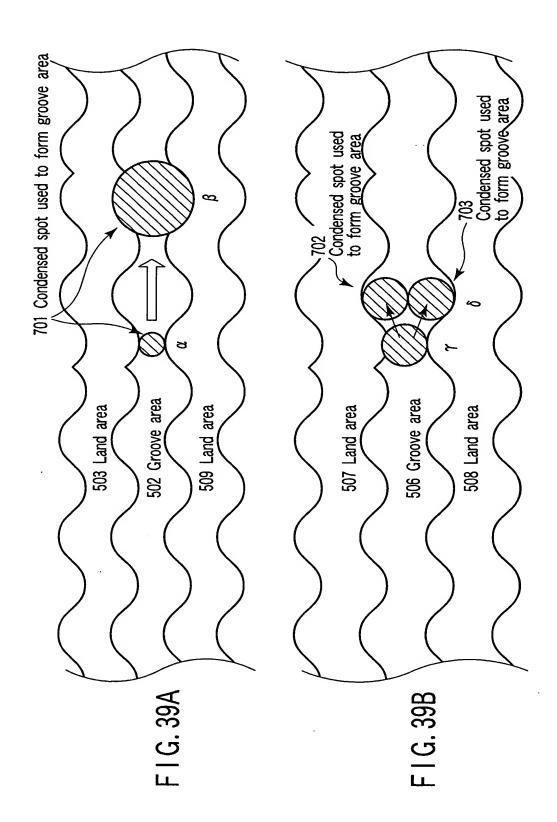
F. G. 3

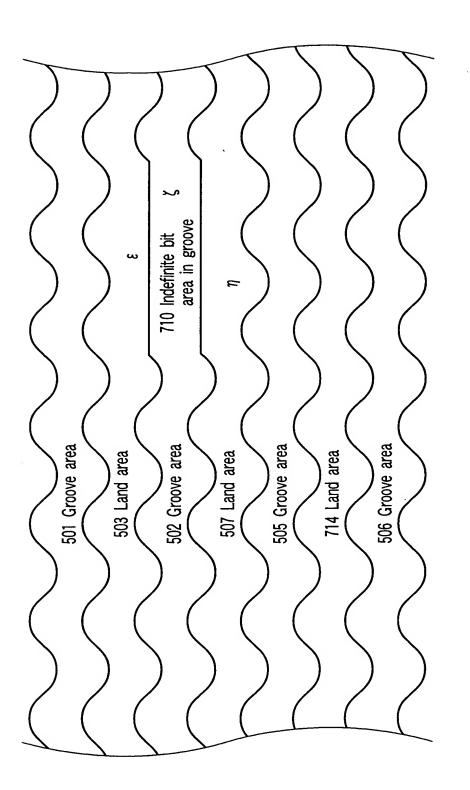


F 1 G. 3/

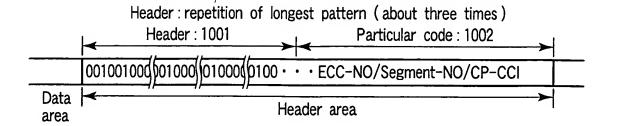
L/G identification	Track number	Track number information A611	Track number information B612
Groove	2n+3	2n+4	2n+3
Land	2n+3	Indefinite (2n+2 or 2n+3)	2n+3
Groove	2n+2	2n+2	2n+3
Land	2n+2	2n+2	Indefinite (2n+1 or 2n+3)
Groove	2n+1	2n+2	2n+1
Land	2n+1	Indefinite (2n or 2n+2)	2n+1
Groove	2n	2n	2n+1

FIG. 38





F I G. 40



F I G. 41

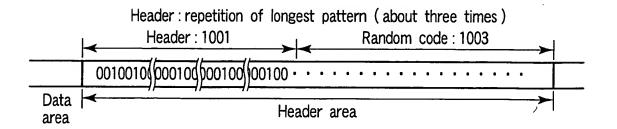


FIG. 42

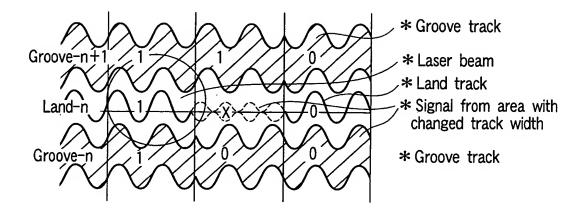


FIG. 43

Track form	Detected track number
Groove	n+3
Even-land (n+2)	(n+2) or (n+3)
Groove	n+2
Odd-land (n+1)	(n+1) or (n+2)
Groove	n+1
Even-land (n)	(n) or (n+1)
Groove	n

FIG. 44

G / L track	Track number	Track number determination criteria
Groove : G(n+2)	1101	
Odd-land :L(n+1)	110*	Only 1101 or 1100 for odd-lands
Groove : G(n+1)	1100	
Even-land : L(n)	* 100	Only 1100 or 0100 for even-lands
Groove : G(n)	0100	

F I G. 45

			-	8-kbyte data block
Groove track	SYNC	Zn · TR-NO(n+2) · Sn	SYNC	Zn·TR-NO(n+2)·Sn+1
Land track	0	dd-land track		
Groove track	SYNC	$Zn \cdot TR-NO(n+1) \cdot Sn$	SYNC	Zn • TR-NO(n+1) • Sn+1
Land track				
Groove track	SYNC	Zn · TR-NO(n) · Sn	SYNC	Zn·TR-NO(n)·Sn+1
		Header a	area	Data area

FIG. 46

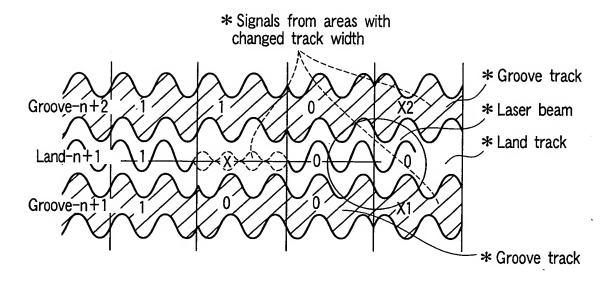


FIG. 47

Groove track	G-S 1101	L-S 11X1	G-S 1101	L-S 11X1	
Land track		L-S 1101		L-S 1100	Γ
Groove track	G-S 1100	L-S X100	G-S 1100	L-S X100	
Land track		L-S 0101		L-S 0100	
Groove track	G-S 0101	L-S 010X	G-S 0100	L-S 010X	

FIG. 48

G / L track	Track number	Track number determination criteria
Groove : G(n+2)	1101X	For grooves, only leading 4 bits are effective
Odd-land : L(n+1)	110X0	Only 11010 or 11000 for odd-lands
Groove : G(n+1)	1100X	For grooves, only leading 4 bits are effective
Even-land : L(n)	X1001	Only 11001 or 01001 for even-lands
Groove : G(n)	0100X	For grooves, only leading 4 bits are effective

F I G. 49

			-	8-kbyte data block
Groove track	SYNC	$Zn \cdot TR-NO(n+2) \cdot Sn$	SYNC	$Zn \cdot TR-NO(n+2) \cdot Sn+1$
Land track				
Groove track	SYNC	$Zn \cdot TR-NO(n+1) \cdot Sn$	SYNC	Zn·TR-NO(n+1)·Sn+1
Land track			<u> </u>	
Groove track	SYNC	Zn · TR-NO(n) · Sn	SYNC	Zn · TR-NO(n) · Sn+1
		Header	area	Data area

FIG. 50

विकास भारत है। जिल्ला के स्थापन के किस क

				8-kbyte data block	_
					T
Groove track Tn+2	Tn+2	Zn·Sn·Zn·Sn	Tn+2	Tn+2 Zn·Sn+1·Zn·Sn+1	Tn+2
Land track	Tn+*	Zn·Sn·Zn·Sn	*+u_	Tn+* Zn·Sn+1·Zn·Sn+1	Tn+*
Groove track Tn+1	Tn+1	Zn · Sn · Zn · Sn	Tn+1	Tn+1 Zn·Sn+1·Zn·Sn+1	Tn+1
Land track	Tn+ *	Zn · Sn · Zn · Sn	*+u1	Tn+* Zn·Sn+1·Zn·Sn+1	+ *
Groove track Tn	Tn	Zn·Sn·Zn·Sn	Tn	Zn·Sn+1·Zn·Sn+1	드
			Header area	◆ Data area	

F1G.51